

**SREE VIDYANIKETHAN ENGINEERING COLLEGE  
(AUTONOMOUS)**

Sree Sainath Nagar, A. Rangampet -517 102.

---

SVEC/EC/2012-13/203

07.09.2012

**NOTICE**

The M.Tech II Semester students are informed that the "II Semester Regular/Supplementary and I Semester Supplementary Examinations September 2012" (Autonomous) (SVEC-10) scheduled to be commenced from 10.09.2012 are postponed to 17.09.2012. The Revised Time Table is placed on the notice boards of the departments and examination portal of the college.

  
**PRINCIPAL**  


- Copy to:
- 1) HODs : EEE, ECE, CSE, IT & BOT with a request to display on the department Notice Boards.
  - 2) The Controller of Examinations
  - 3) ACE, PG Courses; ACE, Technical.
  - 4) Notice Boards
  - 5) A.O: for information and necessary transport arrangement
  - 6) SVEC Canteen, Estate Manager, Hostel Notice Boards