

**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
**(AUTONOMOUS)**  
Sree Sainath Nagar, A. Rangampet – 517 102

---

SVEC/EC/2012-13/203

29.01.2013

**NOTICE**

**The M.Tech I Semester students are informed that the Mid Term-I Internal Examinations scheduled on 29.01.2013 is postponed to 04.02.2013 with the same Timings.**

  
**PRINCIPAL**  
Y. S. B.

- Copy to:**
- 1 The HODs EEE,ECE,CSE,IT & BOT – with a request to display on the Notice Board.**
  - 2 Examination Section.**
  - 3 Notice Boards.**
  - 4 A.O:-for Information and necessary Transport Arrangement.**