

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2012-13/203

14.02.2013

NOTICE

The 2nd and 3rd B.Tech II semester students (SVEC10) are informed that, the MID-1 Internal Examinations scheduled from 25.02.2013 (as per the academic calendar) are postponed to 28.02.2013.


PRINCIPAL

- Copy to: 1) All the HOD's except MCA - with a request to arrange to display on the department Notice Board.**
2) The Controller of Examinations.
3) The A.O:- for information.