

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2012-13/203

08.03.2013

NOTICE

The 1st B.Tech students (SVEC10) are informed that, the MID-2 Internal Examinations scheduled on 12.03.2013 (i.e. Tuesday) and 13.03.2013 (i.e. Wednesday) are postponed as per the schedule given below with the same timing as informed earlier (i.e. 03:00 PM TO 04:30 PM).

S.No	Date of Examination	
	Originally scheduled	Postponed to
1	12.03.2013 TUESDAY	23.03.2013 SATURDAY
2	13.03.2013 TUESDAY	25.03.2013 MONDAY


PRINCIPAL


- Copy to: 1) All the HOD's except MCA - with a request to arrange to display on the department Notice Board.
2) The Controller of Examinations.
3) The A.O:- for information and necessary transport arrangements.