

**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
(AUTONOMOUS)  
Sree Sainath Nagar, A. Rangampet – 517 102

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SVEC/EC/2013-14/203

05.08.2013

**NOTICE**

The M.Tech students are informed that the II Semester Mid-I internal Examinations Scheduled from 05.08.2013 to 12.08.2013 is postponed. The revised dates will be notified later.

  
PRINCIPAL  


- Copy to: 1 HODs : EEE,ECE,CSE,IT & BOT- with a request to display on the department Notice Board.
- 2 Examination Section.
- 3 Notice Boards.