

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)


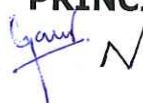
Sree Sainath Nagar, A. Rangampet – 517 102

SVEC/EC/2013-14/203

14.02.2014

NOTICE

In view of Bandh call, the M.Tech I semester and MCA I semester (SVEC 10) internal Examinations scheduled on 14/02/2014 are postponed to 17/02/2014 (i.e. Monday) with the same timings.


PRINCIPAL


- Copy to:**
- 1 The HODs: EEE,ECE,CSE,IT,BOT & MCA – with a request to display on the Notice Board.**
 - 2 Examination Section.**
 - 3 Notice Boards.**
 - 4 A.O:-for Information.**