

# SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet – 517 102

SVEC/EC/2013-14/203

02.09.2014

## NOTICE

**Due to Administrative reasons, the M.Tech II Semester (SVEC10) Regular and Supplementary Examinations scheduled on 08/09/2014 are postponed to 15/09/2014 (i.e. Monday). However, there is no change in the timings.**



**CHIEF CONTROLLER OF EXAMINATIONS**



- Copy to:**
- 1 The Director, SVEC.**
  - 2 The HODs EEE,ECE,CSE,IT & BOT – with a request to display on the Notice Board and circulate among faculty & staff to discharge Examination duty on the rescheduled date.**
  - 3 Examination Section.**
  - 4 Notice Boards.**
  - 5 A.O:-for Information.**
  - 6 Placement officer, SVEC.**