

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)


Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2014-15/203

02.09.2014

NOTICE

Due to Administrative reasons, the IV B.Tech I semester (SVEC10) MID-1 Internal Examinations scheduled on 06.09.2014 (i.e Saturday) are postponed to 15.09.2014 (i.e Monday). However, there is no change in the timings.


CHIEF CONTROLLER OF EXAMINATIONS

- Copy to:**
- 1) The Director, SVEC.**
 - 2) All the HOD's except MCA - with a request to arrange to display on the department Notice Board.**
 - 3) Examination Section**
 - 4) Notice Boards.**
 - 5) The A.O:- for information.**
 - 6) Placement Officer, SVEC.**