

**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
**(AUTONOMOUS)**

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2015-16/203

01.10.2015

**CIRCULAR**

The II B.Tech I semester students (**SVEC-14 Regulations**) are informed that they are required to submit the **II B.Tech I semester Regular (SVEC-14)** Examination registration forms along with the **Original challan** generated from **NIVA software** at the **Examination Section** as per the schedule given below.

S. No.	Scheduled on	Year & Sem	Branch	Time
1	II B.Tech I Sem Regular ( <b>SVEC-14</b> )	<b>12.10.2015</b> <b>MONDAY</b>	<b>CE-A &amp; B</b>	10:30 AM TO 11:20 AM
			<b>EEE-A &amp; B</b>	11:20 AM TO 12:10 PM
			<b>EEE- C &amp; D</b>	12:10 PM TO 01:00 PM
			<b>ME- A, B &amp; C</b>	02:15 PM TO 03:05 PM
			<b>ECE-A &amp; B</b>	03:05 PM TO 03:55 PM
			<b>ECE-A &amp; B</b>	03:55 PM TO 04:45 PM
2	II B.Tech I Sem Regular ( <b>SVEC-14</b> )	<b>13.10.2015</b> <b>TUESDAY</b>	<b>CSE-A &amp; B</b>	10:30 AM TO 11:20 AM
			<b>CSE-C &amp; D</b>	11:20 AM TO 12:10 PM
			<b>EIE- A &amp; B</b>	12:10 PM TO 01:00 PM
			<b>IT- A &amp; B</b>	02:15 PM TO 03:05 PM
			<b>CSSE-A &amp; B</b>	03:05 PM TO 03:55 PM

  
**PRINCIPAL**  


- Copy to:** 1) The HODs of all the Departments (Except M.C.A & GEBH) with a request to arrange to read over in the respective classes and circulate among the staff.  
2) The Controller of Examinations  
3) Notice Boards