

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2015-16/203

07.10.2015

CIRCULAR

The III & IV B.Tech I semester students (**SVEC-10 Regulations**) are informed that they are required to submit the **III & IV B.Tech I semester Regular (SVEC-10)** Examination registration forms along with the **Original challan** generated from **NIVA software** at the **Examination Section** as per the schedule given below.

S. No.	Scheduled on	Year & Sem	Branch	Time
1	III B.Tech I Sem Regular (SVEC-10)	26.10.2015 MONDAY	CE- A & B	08:50 AM TO 09:40 AM
			EEE- A, B & C	09:40 AM TO 10:30 AM
			ME- A, B & C	11:00 AM TO 11:50 AM
			ECE- A, B & C	11:50 AM TO 12:40 PM
			CSE- A, B & C	12:40 PM TO 01:30 PM
			EIE- A & B	02:15 PM TO 03:05 PM
			IT- A & B	03:05 PM TO 03:55 PM
			CSSE- A & B	03:55 PM TO 04:45 PM
2	IV B.Tech I Sem Regular (SVEC-10)	27.10.2015 TUESDAY	CE- A & B	08:50 AM TO 09:40 AM
			EEE- A, B & C	09:40 AM TO 10:30 AM
			ME- A & B	11:00 AM TO 11:50 AM
			ECE- A & B	11:50 AM TO 12:40 PM
			CSE- A & B	12:40 PM TO 01:30 PM
			EIE- A & B	02:15 PM TO 03:05 PM
			IT- A & B	03:05 PM TO 03:55 PM
			CSSE- A & B, EConE	03:55 PM TO 04:45 PM


PRINCIPAL


- Copy to:** 1) The HODs of all the Departments (Except M.C.A & GEBH) with a request to arrange to read over in the respective classes and circulate among the staff.
2) The Controller of Examinations
3) Notice Boards.