

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)



Sree Sainath Nagar, A. Rangampet – 517 102

SVEC/EC/2015-16/203

02.02.2016

NOTICE

Due to Administrative reasons, M.Tech I semester Mid II Examination scheduled on 06/02/2016 (Saturday) is postponed to 09/02/2016 (Tuesday) with the same timings.


PRINCIPAL


- Copy to:
- 1 The HODs of EEE, ECE & CSE – with a request to display on the Notice Board and **instruct the faculty to attend the invigilation duty as per the revised Schedule.**
 - 2 Examination Section.
 - 3 Notice Boards.
 - 4 S.A.O:-for Information.

ACE-3
for n/a
