

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet - 517 102

SVEC/EC/2015-16/203

03.02.2016

NOTICE

Due to Bandh call, the II B.Tech II Semester (SVEC-14) Mid-I Examinations scheduled on 03/02/2016 (Wednesday) is postponed to 09/02/2016 (Tuesday) with the same timings.


PRINCIPAL


- Copy to:
- 1 The Director, SVEC.
 - 2 HODs of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE & GEBH - with a request to display on the Notice Board.
 - 3 Examination Section.
 - 4 Notice Boards.
 - 5 S.A.O:-for Information necessary transport arrangement.