

**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
**(AUTONOMOUS)**

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2016-17/203

17.10.2016

**CIRCULAR**

The III B.Tech I semester students (**SVEC-14 Regulations**) are informed that they are required to submit the **III B.Tech I semester Regular (SVEC-14)** Examination registration forms along with the **Original challan** generated from **NIVA** at the **Examination Section** as per the schedule given below.

S. No.	Year & Sem	Scheduled on	Branch	Time
1	III B.Tech I Sem Regular ( <b>SVEC-14</b> )	<b>26.10.2016</b> <b>WEDNESDAY</b>	<b>CE- A &amp; B</b>	08:50 AM TO 09:40 AM
			<b>EEE- A &amp; B</b>	09:40 AM TO 10:30 AM
			<b>EEE- C &amp; D</b>	11:00 AM TO 11:50 AM
			<b>ME- A, B &amp; C</b>	11:50 AM TO 12:40 PM
			<b>ECE- A &amp; B</b>	12:40 PM TO 01:30 PM
2	III B.Tech I Sem Regular ( <b>SVEC-14</b> )	<b>27.10.2016</b> <b>THURSDAY</b>	<b>ECE- C &amp; D</b>	08:50 AM TO 09:40 AM
			<b>CSE- A &amp; B</b>	09:40 AM TO 10:30 AM
			<b>CSE- C &amp; D</b>	11:00 AM TO 11:50 AM
			<b>CSSE- A &amp; B</b>	11:50 AM TO 12:40 PM
			<b>EIE- A &amp; B ,</b> <b>IT- A &amp; B</b>	12:40 PM TO 01:30 PM

  
**PRINCIPAL**  


- Copy to:** 1) The HODs of all the Departments (Except M.C.A & GEBH) with a request to arrange to read over in the respective classes and circulate among the staff members.  
2) The Controller of Examinations  
3) Notice Boards.