

**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
**(AUTONOMOUS)**

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2016-17/203

17.10.2016

**CIRCULAR**

The IV B.Tech I semester students (**SVEC-10 Regulations**) are informed that they are required to submit the **IV B.Tech I semester Regular (SVEC-10)** Examination registration forms along with the **Original challan** generated from **NIVA** at the **Examination Section** as per the schedule given below.

S. No.	Year & Sem	Scheduled on	Branch	Time
1	IV B.Tech I Sem Regular ( <b>SVEC-10</b> )	<b>28.10.2016</b> <b>FRIDAY</b>	<b>CE- A &amp; B</b>	08:50 AM TO 09:40 AM
			<b>EEE- A , B &amp; C</b>	09:40 AM TO 10:30 AM
			<b>ME- A, B &amp; C</b>	11:00 AM TO 11:50 AM
			<b>ECE- A , B &amp; C</b>	11:50 AM TO 12:40 PM
			<b>EIE- A &amp; B</b>	12:40 PM TO 01:30 PM
2	IV B.Tech I Sem Regular ( <b>SVEC-10</b> )	<b>29.10.2016</b> <b>SATURDAY</b>	<b>CSE- A , B &amp; C</b>	08:50 AM TO 09:40 AM
			<b>IT- A &amp; B</b>	09:40 AM TO 10:30 AM
			<b>CSSE-A &amp; B</b>	11:00 AM TO 11:50 AM

  
**PRINCIPAL**

- Copy to:** 1) The HODs of all the Departments (Except M.C.A & GEBH) with a request to arrange to read over in the respective classes and circulate among the staff members.  
2) The Controller of Examinations  
3) Notice Boards.