

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2016-17/203

17.10.2016

CIRCULAR

The II & III *B.Tech* students (**SVEC-14 Regulations**) are informed that they are required to submit the **II B.Tech I & II semester (SVEC-14)** Supplementary and **I B.Tech (SVEC-14)** Supplementary Examination Registration forms along with the **Original challan** generated from **NIVA** at the **Examination Section** as per the schedule given below.

S. No.	Scheduled on	Year & Sem	Branch	Time
1	31.10.2016 MONDAY	II B.Tech I Semester (SVEC-14)	All Branches	09: 00 AM to 11:00 AM
2		II B.Tech II Semester (SVEC-14)	All Branches	11: 00 AM to 01:00 PM
3		I B.Tech (SVEC-14)	All Branches	02: 00 PM to 05:00 PM


PRINCIPAL

- Copy to:** 1) The HODs of all the Departments (Except M.C.A & GEBH) with a request to arrange to read over in the respective classes and circulate among the staff members.
2) The Controller of Examinations
3) Notice Boards.