

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.


SVEC/EC/2016-17/203

11.05.2017

CIRCULAR

The I B.Tech II Semester students (SVEC-16 Regulations) are informed that they are required to submit the I B.Tech II Semester (SVEC-16) Regular and I B.Tech I Semester (SVEC-16) Supplementary Examination registration forms along with the **Original challan** generated from **NIVA software** at the **Examination Section** as per the schedule given below.

S. No.	Scheduled on	Year & Semester	Branch	Time
1	22.05.2017 MONDAY	I B.Tech II Semester Regular (SVEC-16)	CE- A & B	09:40 AM TO 10:30 AM
			EEE- A & B	10:30 AM TO 11:20 AM
			EEE- C & D	11:20 AM TO 12:10 PM
			CSSE- A & B	12:10 PM TO 01:00 PM
			ECE- A & B	02:15 PM TO 03:05 PM
			ECE- C & D	03:05 PM TO 03:55 PM
2	23.05.2017 TUESDAY	I B.Tech II Semester Regular (SVEC-16)	CSE- A & B	09:40 AM TO 10:30 AM
			CSE- C & D	10:30 AM TO 11:20 AM
			EIE- A & B	11:20 AM TO 12:10 PM
			IT- A & B	12:10 PM TO 01:00 PM
			ME- A , B & C	02:15 PM TO 03:05 PM
3	24.05.2017 WEDNESDAY	I B.Tech I Semester Supplementary (SVEC-16)	CE & EEE	09:40 AM TO 10:30 AM
			ME & EIE	10:30 AM TO 11:20 AM
			ECE & IT	11:20 AM TO 12:10 PM
			CSE & CSSE	12:10 PM TO 01:00 PM


PRINCIPAL

- Copy to:** 1) The Director, SVEC.
2) The HODs of all the Departments (Except M.C.A) with a request to arrange to read over in the respective classes and circulate among the staff.
3) The Controller of Examinations
4) Notice Boards.