

# SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet – 517 102

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SVEC/EC/2017-18/203

23.08.2017

## NOTICE

Due to Administrative reasons, the M.Tech II semester (SVEC 16) Regular Examination scheduled on 26/08/2017 (Saturday) is postponed to 31/08/2017 (Thursday) with the same timings.

  
PRINCIPAL  
*Yand.* 

- Copy to:
- 1 The Director, SVEC
  - 2 The HODs of CSE, EEE, ECE & IT- with a request to display on the Notice Board and **instruct the faculty to attend the invigilation duty as per the revised Schedule.**
  - 3 Examination Section.
  - 4 Notice Boards.
  - 5 S.A.O:-for Information.