

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2018-19/203

10.09.2018

NOTICE

Due to Bharat Bundh, the II B.Tech I semester Mid-1 Internal Examinations for **2018 admitted lateral entry students** scheduled on 10.09.2018 (i.e Monday) is postponed to 11.09.2018 (i.e Tuesday) with the same timings circulated earlier.


PRINCIPAL

- Copy to: 1) The Director, SVEC.
2) All the HOD's (except MCA) - with a request to arrange to display on the department Notice Board.
3) The Controller of Examinations.
4) The S.A.O:- for information.