

**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2019-20/203

19.08.2019

**NOTICE**

Due to Administrative reasons, the II, III & IV B.Tech I semester (SVEC-16) MID-1 Internal Examinations scheduled on **24.08.2019 (i.e Saturday)** are postponed to **27.08.2019 (i.e Tuesday)** with the same timings circulated earlier. However the class work on **24.08.2019** will be conducted with Saturday's time table.

  
PRINCIPAL  


- Copy to: 1) The Director, SVEC.  
2) The Dean, Academics.  
3) The Dean, Placements & Training.  
4) All the HOD's except BS&H & MCA - with a request to arrange to display on the department Notice Board and informed to the faculty members and staff to discharge their duties on 27.08.2019 instead of 24.08.2019.  
5) Examination Section.  
6) Notice Boards.  
7) The S.A.O:- for information and necessary transport arrangements.