

# **SREE VIDYANIKETHAN ENGINEERING COLLEGE**

**(AUTONOMOUS)**

**Sree Sainath Nagar, A. Rangampet -517 102.**

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SVEC/EC/2019-20/203

09.10.2019

## **NOTICE**

Due to Administrative reasons, the MID-1 Internal Examinations for I B.Tech I semester students (SVEC-19) scheduled to be commenced from 15.10.2019 are **postponed to 22.10.2019** with the same timings.

  
**PRINCIPAL**  


- Copy to: 1) The Director, SVEC.  
2) The Vice-Principal.  
3) The Dean, Acedemics.  
4) All the HOD's except MCA - with a request to arrange to display on the department Notice Board.  
5) The Controller of Examinations.  
6) The S.A.O:- for information.  
7) NIVA office.