



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2019-20/203

17.03.2020

NOTICE

As per the orders of the government, to prevent students from exposure to COVID-19, the IV B.Tech II semester Mid-2 Internal Examinations scheduled from 26.03.2020 (i.e Thursday) are postponed until further notice. The rescheduled date will be intimated later.


PRINCIPAL


Copy to : The Vice-Principal, SVEC.

The Dean, Academics, SVEC.

The HOD's of CE, ME, EEE, ECE, CSE, EIE, IT, CSSE and BS& H: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Controller of Examinations.

The S.A.O:- for necessary transport arrangement.