

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2020-21/203

17.11.2020

NOTICE

In view of placement drive, the IV B.Tech I semester (SVEC-16) students are informed that the III B.Tech II semester (SVEC-16) MID-2 Internal Examinations scheduled on **18.11.2020, 20.11.2020 & 21.11.2020** are postponed as detailed below with the same timings circulated earlier.

S.No	Originally Scheduled	Postponed to
1	18.11.2020 (Wednesday)	27.11.2020 (Friday)
2	20.11.2020 (Friday)	28.11.2020 (Saturday)
3	21.11.2020 (Saturday)	30.11.2020 (Monday)


PRINCIPAL


- Copy to: 1) The Vice-Principal, SVEC.
2) The Dean, Academics.
3) The Dean, Placements & Training.
4) All the HOD's except MCA - with a request to arrange to display on the department Notice Board and inform to the faculty to discharge their invigilation duties according to the revised schedule shown above.
5) The S.A.O, for information and necessary transport arrangement.
6) Examination Section.
7) The Office, ISA.
8) Notice Boards.