

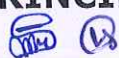
SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)
Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2020-21/203

02.02.2021

NOTICE

Due to GATE-2021 Examinations, the MID-2 Internal Examinations for III & IV B.Tech I Semester (SVEC-16) students scheduled on 5th & 6th February 2021 are postponed to **8th & 9th February 2021** respectively. However there is no change in the timing of Examination.

P. C. Chalapathi
PRINCIPAL


- Copy to:
- 1) The Vice-Principal, SVEC.
 - 2) The Dean, Academics.
 - 3) The Dean, Placements & Training.
 - 4) All the HOD's of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE & BS&H - with a request to arrange to display on the department Notice Board and inform to the faculty to discharge their invigilation duties according to the revised schedule.
 - 5) The S.A.O, for information and necessary transport arrangement.
 - 6) Examination Section.
 - 7) The Office, ISA.
 - 8) Notice Boards.

ACE-2
for ma
02/2/2021