

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2020-21/203

07.07.2021

NOTICE

In view of COVID Pandemic situation, the **MID-2** Internal Examinations and **Semester-end Regular** Examinations for **II & III B.Tech II Semester** students (2018 & 2019 Admitted Batches) scheduled to be commenced from **12th July 2021** are postponed and the new dates of examinations will be intimated later.


PRINCIPAL


- Copy to:
- 1) The Director, SVEC.
 - 2) The Vice-Principal, SVEC.
 - 3) The Dean, Academics.
 - 4) The Dean, Placements & Training.
 - 5) All the HOD's of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE & BS&H - with a request to arrange to display on the department Notice Board and inform to the faculty to discharge their invigilation duties according to the revised schedule.
 - 6) The S.A.O, for information and necessary transport arrangement.
 - 7) Examination Section.
 - 8) The Office, ISA.
 - 9) Notice Boards.