

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2020-21/203

20.09.2021

NOTICE

In view of "Accenture and Infosys Company Specific Training", the MID-1 Internal Examinations for M.C.A II Semester students (2020 Admitted Batch) scheduled to be commenced from 21st September 2021 are postponed to 04th October 2021 with the same timings.

PRINCIPAL

Copy to: 1) The Director, Academics

- 2) The Vice-Principal, SVEC.
- 3) The Dean, Academics.
- 4) The HOD of M.C.A with a request to arrange to display on the department Notice Board and inform to the students as well as faculty.
- 5) The S.A.O, for information and necessary transport arrangement.
- 6) Examination Section.
- 7) The Office, ISA.
- 8) Notice Boards.