



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2020-21/203

21.09.2021

NOTICE

The Students of I B.Tech II semester are advised to note the changes in the timings of Semester-end Regular Examinations scheduled from **28.09.2021 (Tuesday)**.

S.No.	Year & Sem	Scheduled Time	Re-scheduled Time
1.	I B.Tech II Semester Regular Examinations	02.00 P.M To 05.00 P.M	10.00 A.M To 01.00 P.M


PRINCIPAL


Copy to : The Director, Academics, SVEC.

The Vice-Principal, SVEC.

The Dean, Academics, SVEC.

The HOD's of CE, ME, EEE, ECE, CSE, EIE, IT, CSSE and BS& H: with a request to arrange to display on the department Notice Board and inform to the faculty to discharge their invigilation duties according to the revised schedule.

The Controller of Examinations.

The S.A.O:- for necessary transport arrangement.