



# SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2022-23/203

03.09.2022

## NOTICE

Due to Placement drive, the **MID-1 Internal Examinations** for IV B.Tech I Semester (SVEC-19) students of **EEE, ECE, CSE, EIE, IT & CSSE** branches scheduled on **05.09.2022 (Monday)** are postponed to **12.09.2022 (Monday)** with the same timings circulated earlier. The IV B.Tech I Semester students of **EEE, ECE, CSE, EIE, IT & CSSE** branches are required to note the change in the schedule of the examination.

**PRINCIPAL**

- Copy to:
- 1) The Director, Academics & Research, SVEC.
  - 2) The Vice-Principal, SVEC.
  - 3) The Dean, Academics.
  - 4) The Dean, Placements & Training.
  - 5) All the HOD's of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE & BS&H - with a request to arrange to display on the department Notice Board and inform to the faculty to discharge their invigilation duties according to the revised schedule.
  - 6) The S.A.O, for information and necessary transport arrangement.
  - 7) Examination Section.
  - 8) The Office, ISA.
  - 9) Notice Boards.