



**SREE VIDYANIKETHAN ENGINEERING COLLEGE**  
(AUTONOMOUS)  
SREE SAINATH NAGAR, A. RANGAMPET – 517 102

**NOTICE**

10.04.2014

**It is informed to the students who have applied for Recounting/ Revaluation/ Personal Verification of answer scripts of II B.Tech II Semester Supplementary Examinations January 2014, that their marks details after recounting/ Revaluation/ Personal Verification are furnished hereunder.**

**II B.Tech II Semester Supplementary Examinations January 2014**

SL. No	SUBJECT	HT.No	MARKS AFTER RE-VALUATION
1	STRUCTURAL ANALYSIS-I (10BT40101)	11121A0157	<b>29</b>
2	SWITCHING THEORY AND LOGIC DESIGN(10BT40404)	11121A0446	<b>21</b>
3	CONTROL SYSTEMS (10BT41301)	10121A0478	<b>23</b>
4	PULSE AND DIGITAL CIRCUITS (10BT40402)	10121A1019	<b>16</b>
5	THEORY OF COMPUTATION (10BT60501)	11121A1210	<b>20</b>
6		11121A1234	<b>16</b>

**For all the remaining students who have applied for Recounting/ Revaluation/ Personal Verification for the above examinations, there is no change in the marks after Recounting/ Revaluation/ Personal Verification.**

PRINCIPAL