



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

SREE SAINATH NAGAR, A. RANGAMPET – 517 102

NOTICE

22.02.2017

It is informed to the students who have applied for Recounting/ Revaluation/ Personal Verification of answer scripts of II B.Tech I Semester (SVEC-10) Supplementary Examinations November 2016, that their marks details after recounting/ Revaluation/ Personal Verification are furnished hereunder.

II B.Tech I Semester (SVEC-10) Supplementary Examinations November 2016

SL. No	SUBJECT	HT.No	MARKS AFTER RE-VALUATION / RE-COUNTING / PERSONAL VERIFICATION
1	STRENGTH OF MATERIALS-10BT30301	12121A0390	34
2	DIGITAL LOGIC DESIGN-10BT30422	13121A1233	21
3		12121A1564	28
4	DISCRETE MATHEMATICAL STRUCTURES-10BT30501	12121A0518	22
5	DATA STRUCTURES-10BT30502	13121A0571	26
6	CIRCUIT THEORY-10BT30223	11121A1003	19
7		12121A1068	34
8		14125A1002	28
9	THERMODYNAMICS & FLUID MECHANICS-10BT31001	14125A1002	21
10	SEMICONDUCTOR DEVICES AND CIRCUITS-10BT30401	12121A1336	34

For all the remaining students who have applied for Recounting/ Revaluation/ Personal Verification for the above examinations, there is no change in the marks after Recounting/ Revaluation/ Personal Verification.

P.C.K. Chalapathi
PRINCIPAL
22/2/2017