

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

SREE SAINATH NAGAR, A. RANGAMPET – 517 102

NOTICE

22.02.2017

It is informed to the students who have applied for Recounting/ Revaluation/ Personal Verification of answer scripts of II B.Tech II Semester (SVEC-14) Supplementary Examinations December 2016, that their marks details after recounting/ Revaluation/ Personal Verification are furnished hereunder.

II B.Tech II Semester (SVEC-14) Supplementary Examinations December 2016

SL. No	SUBJECT	HT.No	MARKS AFTER RE-VALUATION / RE-COUNTING / PERSONAL VERIFICATION
1	14BT40204-TRANSFORMERS AND INDUCTION MACHINES	14121A02N6	31
2	14BT40402-ELECTRONIC CIRCUIT ANALYSIS AND DESIGN	14121A04M3	17
3	14BT40405-SWITCHING THEORY AND LOGIC DESIGN	14121A04M3	13
4		15125A0403	29
5		15125A0412	24
6	14BT40431-ANALOG ELECTRONIC CIRCUITS	14121A02N6	32
7	14BT40432-DATA COMMUNICATIONS	14121A1252	29

For all the remaining students who have applied for Recounting/ Revaluation/ Personal Verification for the above examinations, there is no change in the marks after Recounting/ Revaluation/ Personal Verification.

P. C. Chalapathi
PRINCIPAL
®