

# SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh


## NOTICE

Date : 22/02/2018

It is informed to the students who have applied for Recounting/ Revaluation/ Personal Verification of answer scripts of I B.Tech II Semester (SVEC-16) Supplementary Examinations, December 2017, that their marks details after recounting/ Revaluation/ Personal Verification are furnished hereunder.

### I B.Tech II Semester (SVEC-16) Supplementary Examinations, December 2017

S. No.	Subject Name With Code	Roll Number	Marks after Revaluation/ Recounting/ Personal Verification
1	16BT20102-Engineering Mechanics	16121A0396	29
2		16121A03C0	29
3	16BT21501-Digital Logic Design	16121A1290	31
4	16BT2BS01-Transformation Techniques and Partial Differential Equations	16121A1529	14

  
PRINCIPAL  
