

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

NOTICE


Date : 25.01.2020

It is informed to the students who have applied for Recounting/ Revaluation/ Personal Verification of answer scripts of III B.Tech I Semester (SVEC-16) Regular Examinations, November 2019, that their marks details after recounting/ Revaluation/ Personal Verification are furnished hereunder.

III B.Tech I Semester (SVEC-16) Regular Examinations, November 2019

S. No.	Subject Name With Code	Roll Number	Marks after Revaluation/ Recounting/ Personal Verification
1	16BT30503-PYTHON PROGRAMMING	17121A1506	22
2	16BT3HS02-MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY	17121A04H3	56
3	16BT50104-STRUCTURAL ANALYSIS - II	17121A0118	33
4	16BT50201-CONTROL SYSTEMS	17121A02C4	34
5		18125A0444	30
6	16BT50202-POWER ELECTRONICS	17121A0237	12
7		17121A0248	30
8		17121A0264	32
9		17121A02A4	30
10		18125A0241	31
11		18125A0245	34
12		18125A0251	28
13	16BT50203-SYNCHRONOUS MACHINES	17121A02A4	33
14		17121A02B9	34
15		17121A02K3	42
16	16BT50204-TRANSMISSION AND DISTRIBUTION	17121A02E0	32
17	16BT50402-MICROPROCESSORS AND MICROCONTROLLERS	17121A0476	56
18		17121A04H3	22
19	16BT50403-VLSI DESIGN	17121A04H3	6

S. No.	Subject Name With Code	Roll Number	Marks after Revaluation/ Recounting/ Personal Verification
20	16BT50501-COMPUTER NETWORKS	14121A1548	35
21		17121A0476	59
22		17121A04A2	59
23		17121A05K6	8
24		17121A1585	30
25		17121A1598	28
26		17121A15B5	36
27		17121A15B8	28
28		16BT50502-LINUX PROGRAMMING	17121A0508
29	17121A05B6		28
30	16BT51202-OBJECT ORIENTED ANALYSIS AND DESIGN	17121A1209	39
31		17121A1224	40
32	16BT51501-COMPILER DESIGN	17121A1241	40
33		17121A1275	31


PRINCIPAL
 25/01/2020