SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

NOTICE

Date: 25.01.2020

It is informed to the students who have applied for Recounting/ Revaluation/ Personal Verification of answer scripts of III B.Tech I Semester (SVEC-16) Supplementary Examinations, November 2019, that their marks details after recounting/ Revaluation/ Personal Verification are furnished hereunder.

III B.Tech I Semester (SVEC-16) Supplementary Examinations, November 2019

| S. No. | Subject Name With Code | Roll Number | Marks after Revaluation/ Recounting/ Personal Verification |
|-----------|--|-------------|--|
| 1 | 16BT50104-STRUCTURAL ANALYSIS - II | 16121A0114 | 37 |
| 2 | 16BT50203-SYNCHRONOUS MACHINES | 16121A02J0 | 42 |
| 3 | 16BT50302-INDUSTRIAL ENGINEERING AND MANAGEMENT | 16121A0397 | 40 |
| 4 | | 16121A03E2 | 41 |
| 5 | 16BT50305-THERMAL ENGINEERING-II | 16121A0397 | 33 |
| 6 | 16BT50501-COMPUTER NETWORKS | 16121A1261 | 29 |
| 7 | 16BT51002-INDUSTRIAL INSTRUMENTATION - II | 16121A1047 | 36 |
| 8 | 16BT51004-COMPUTER ORGANIZATION AND ARCHITECTURE | 16121A1001 | 32 |
| 9 | 16BT51202-OBJECT ORIENTED ANALYSIS AND DESIGN | 16121A0568 | 39 |
| 10 | 16BT60404-IMAGE PROCESSING | 16121A1295 | 39 |

