

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, Tirupati, Chittoor, Andhra Pradesh


NOTICE

Date : 24-01-2023

It is informed to the students applied for Recounting/ Revaluation/ Personal Verification of answer scripts of II B.Tech II Semester (SVEC-14) Supplementary Examinations, December-2022 , that their marks details after recounting/ Revaluation/ Personal Verification are furnished hereunder.

II B.Tech II Semester (SVEC-14) Supplementary Exams, December-2022

S. No.	Subject Name With Code	Roll Number	Marks after Revaluation/ Recounting/ Personal Verification
1	14BT40402-ELECTRONIC CIRCUIT ANALYSIS AND DESIGN	15121A1072	34
2	14BT40404-SIGNALS AND SYSTEMS	15121A1072	41


PRINCIPAL
