

SREE VIDYANIKETHAN ENGINEERING COLLEGE

SREE SAINATH NAGAR, A. RANGAMPET - 517 102

2nd SHIFT POLYTECHNIC

SVEC/EC/2011-12/508

15.09.2011

CIRCULAR

The III Semester (C09) Diploma Students are informed that, the **Unit Test-II** Examinations will be conducted as per the following schedule.

DAY/ DATE	TIME	DEEE III SEMESTER		DECE III SEMESTER	
		SUBJECT CODE	SUBJECTS	SUBJECT CODE	SUBJECTS
07.10.2011 Friday	10.00 AM To 11.00 AM	EE-301	English - II	EC-301	English - II
	03.00 PM To 04.00 PM	EE-302	Engineering Mathematics -II	EC-302	Engineering Mathematics -II
08.10.2011 Saturday	10.00 AM To 11.00 AM	EE-303	Electrical Circuits	EC-303	Electronic Circuits -I
	03.00 PM to 04.00 PM	EE-304	DC Machines & Batteries	EC-304	Communication Engineering
10.10.2011 Monday	10.00 AM To 11.00 AM	EE-305	Electrical and Electronic Measuring Instruments	EC-305	Digital Electronics
	03.00 PM to 04.00 PM	EE-306	Electronics Engineering	EC-306	Circuit Theory


PRINCIPAL

- Copy to: 1) The Vice Principal, 2nd Shift Polytechnic: With a request to arrange to read out in the respective classes and to display on the Notice Board
2) A.O. for Information
3) The Exam Section
4) Notice Board