



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

Date: 01-12-2011

M.C.A III Semester Regular Examinations January, 2012

EXAMINATION SCHEDULE

Time: 02.00 PM To 05.00PM

Date/Day	For 2010 Admitted Batch only
16-01-2012 Monday	OPERATIONS RESEARCH 10MC3B501
18-01-2012 Wednesday	WEB APPLICATION DEVELOPMENT 10MC30101
20-01-2012 Friday	COMPUTER NETWORKS 10MC30102
23-01-2012 Monday	DATA WAREHOUSING AND DATA MINING 10MC30103
25-01-2012 Wednesday	SOFTWARE ENGINEERING 10MC30104


PRINCIPAL

Copy to : The Director. M.C.A.

The HOD, M.C.A : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The A.O, S.V.E.C.

SVEC Canteen.

Estate Manager.

Hostel Notice Boards.