



SREE VIDYANIKETHAN ENGINEERING COLLEGE
(Autonomous)
Sree Sainath Nagar, Tirupati- 517 102

Date: 01-12-2011

for M.C.A II Semester Supplementary Examinations January, 2012

EXAMINATION SCHEDULE

Time: 02.00 PM To 05.00PM

Date/Day	For 2010 Admitted Batch only
17-01-2012 Tuesday	PROBABILITY AND STATISTICS 10MC2BS01
19-01-2012 Thursday	COMPUTER ARCHITECTURE AND ORGANIZATION 10MC20101
21-01-2012 Saturday	OPERATING SYSTEMS 10MC20102
24-01-2012 Tuesday	OBJECT ORIENTED PROGRAMMING 10MC20103
27-01-2012 Friday	DATABASE MANAGEMENT SYSTEM 10MC20104

Note: Supplementary laboratory examinations for M.C.A II Semester will be conducted from 11.01.2012 to 12.01.2012 .


PRINCIPAL

Copy to : The Director, M.C.A.

The HOD, M.C.A : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on Notice Board.

The Examination Section.

The A.O, S.V.E.C.

SVEC Canteen.

Estate Manager.

Hostel Notice Boards.