

# KSREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet – 517 102

SVEC/EC/2011-12/A-305

16.03.2012

## CIRCULAR

All the M. Tech. I Semester students are informed that the II- Mid-Term examinations(**Revised Schedule**) will be conducted as per the schedule shown below.

| DATE /DAY       | TIME                     | Bio-Technology (BOT)                                  | Computer Science (CS)                                  | Electrical Power Systems (EPS)                          | Software Engineering(SE)                               | Digital Electronics and Communication Systems (DECS) | VLSI   | Communication Systems (CMS)                                   | Computer Networks and Information Security (CN&IS) |
|-----------------|--------------------------|---|--|---|--|--|--|---|--|
| 22/03/12<br>THU | 2.30 PM<br>TO<br>4.30 PM | General Microbiology and Biochemistry<br>(10MT10301)  | Computer Architecture<br>(10MT10501)                   | Advanced Control Systems<br>(10MT10701)                 | Advanced Data Structures and Algorithms<br>(10MT12501) | Advanced Digital Signal Processing<br>(10MT13801)    | Analog IC Design<br>(10MT15701)                                | Advanced Mathematics for Communication Systems<br>(10MT16101) | Software Security Engineering<br>(10MT22504)       |
| 24/03/12<br>SAT | 2.30 PM<br>TO<br>4.30 PM | Molecular biology<br>(10MT10302)                      | Discrete Structures and Automata Theory<br>(10MT10502) | EHVAC Transmission<br>(10MT10702)                       | Advanced Software Engineering<br>(10MT12502)           | Digital Communication Techniques<br>(10MT13802)      | Digital Design Modeling and Synthesis with HDLs<br>(10MT15702) | Modern Digital Communication Techniques<br>(10MT16102)        | Data warehousing and Data Mining<br>(10MT20502)    |
| 26/03/12<br>MON | 2.30 PM<br>TO<br>4.30 PM | Microbial Engineering<br>(10MT10303)                  | Data Structures and Algorithms<br>(10MT10503)          | Microprocessors & Microcontrollers<br>(10MT10703)       | Middleware Technologies<br>(10MT12503)                 | Digital System Design<br>(10MT13803)                 | Digital IC Design<br>(10MT15703)                               | Detection and Estimation of Signals<br>(10MT23803)            | Data Structures and Algorithms<br>(10MT10503)      |
| 27/03/12<br>TUE | 2.30 PM<br>TO<br>4.30 PM | Immunology<br>(10MT10304)                             | Software Engineering<br>(10MT10504)                    | Power System Control & Stability<br>(10MT10704)         | Software Metrics<br>(10MT12504)                        | Embedded Systems<br>(10MT13804)                      | Hardware Software Co-Design<br>(10MT15704)                     | Computer Communication Networks<br>(10MT16104)                | TCP/IP Protocols<br>(10MT16301)                    |
| 28/03/12<br>WED | 2.30 PM<br>TO<br>4.30 PM | Process Engineering Principles<br>(10MT10305)         | Database Management Systems<br>(10MT10505)             | Reactive Power compensation & Management<br>(10MT10705) | Software Requirements and Estimation<br>(10MT12505)    | Microcomputer System Design<br>(10MT13805)           | VLSI Technology<br>(10MT15705)                                 | Information and Coding techniques<br>(10MT16105)              | Database Security and Privacy<br>(10MT16302)       |
| 29/03/12<br>THU | 2.30 PM<br>TO<br>4.30 PM | Analytical Techniques in Biotechnology<br>(10MT10306) | Software Testing Methodologies<br>(10MT10509)          | Power Quality<br>(10MT10708)                            | Virtualization and Cloud Computing<br>(10MT12508)      | Low Power VLSI Design<br>(10MT13808)                 | ASIC Design<br>(10MT15706)                                     | Radar Signal Processing<br>(10MT16108)                        | Network Management Systems<br>(10MT16303)          |

  
**PRINCIPAL**

- Copy to: 1) The HODs of EEE, ECE, CSE, IT & BOT with a request to circulate among the students and staff and display on the department notice board.  
2) Director, PG Courses  
3) The Examination Section  
4) A.O, for necessary transport arrangement.