## SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

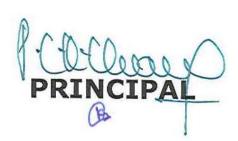
Sree Sainath Nagar, A. Rangampet -517 102.

SVEC/EC/2011-12/203

28.05.2012

## **NOTICE**

In view of A. P. State Bandh on 28.05.2012, all the 2<sup>nd</sup> B.Tech II semester students are informed that the 2<sup>nd</sup> B.Tech II semester (SVEC-10) Regular Examinations (Autonomous) scheduled on 28.05.2012 (i.e. Monday) are postponed to 05.06.2012 (i.e. TUESDAY) with the same timings as informed earlier.



- Copy to: 1) HODs: CIVIL, CSE, CSSE, IT, ECE, EIE, EConE, EEE, BOT & GEBH with a Request to display on the department Notice Boards.
  - The Controller of Examinations
  - Notice Boards
  - 4) A.O: for information and necessary transport arrangement
  - 5) Estate Manager: for information