



# SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapur)

## III B.Tech I Semester Regular Examinations November, 2012

EXAMINATION SCHEDULE FOR SUBSTITUTE SUBJECTS  
FOR STUDENTS OF R09 REGULATION READMITTED TO III B.TECH I SEMESTER (SVEC 10)

Date: 03-10-2012

Time: 10.00 AM To 01.00PM

DATE /DAY	For 2009 Admitted Batch only
	CSE
04/12/2012 TUESDAY	SYSTEM SOFTWARE 10BT51501
08/12/2012 SATURDAY	COMPUTER GRAPHICS 10BT51202

PRINCIPAL

Copy to : The HOD of CSE : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.  
The Examination Section.  
The A.O, S.V.E.C.  
SVEC Canteen. , Estate Manager, Hostel Notice Boards.