# SREE VIDYANIKETHAN ENGINEERING COLLEGE
## (AUTONOMOUS)
Sree Sainath Nagar, A. Rangampet – 517 102

SVEC/EC/2012-13/A-305

11.01.2013

## CIRCULAR

All the M. Tech. I Semester students are informed that the I- Mid-Term examinations will be conducted as per the schedule shown below.

| DATE /DAY | TIME | Bio-Technology | Computer Science (CS) | Electrical Power Systems (EPS) | Software Engineering(SE) | Digital Electronics and Communication Systems (DECS) | VLSI | Communication Systems (CMS) | Computer Networks and Information Security (CN&IS) |
|---|---|---|---|---|---|---|---|---|---|
| 28/01/13 MON | 2.30 PM TO 4.30 PM | General Microbiology and Biochemistry (10MT10301) | Computer Architecture (10MT10501) | Advanced Control Systems (10MT10701) | Advanced Data Structures and Algorithms (10MT12501) | Advanced Digital Signal Processing (10MT13801) | Analog IC Design (10MT15701) | Advanced Mathematics for Communication Systems (10MT16101) | Software Security Engineering (10MT22504) |
| 29/01/13 TUE | 2.30 PM TO 4.30 PM | Molecular biology (10MT10302) | Discrete Structures and Automata Theory (10MT10502) | EHVAC Transmission (10MT10702) | Advanced Software Engineering (10MT12502) | Digital Communication Techniques (10MT13802) | Digital Design Modeling and Synthesis with HDLs (10MT15702) | Modern Digital Communication Techniques (10MT16102) | Data warehousing and Data Mining (10MT20502) |
| 30/01/13 WED | 2.30 PM TO 4.30 PM | Microbial Engineering (10MT10303) | Data Structures and Algorithms (10MT10503) | Microprocessors & Microcontrollers (10MT10703) | Middleware Technologies (10MT12503) | Digital System Design (10MT13803) | Digital IC Design (10MT15703) | Detection and Estimation of Signals (10MT23803) | Data Structures and Algorithms (10MT10503) |
| 31/01/13 THU | 2.30 PM TO 4.30 PM | Immunology (10MT10304) | Software Engineering (10MT10504) | Power System Control & Stability (10MT10704) | Software Metrics (10MT12504) | Embedded Systems (10MT13804) | Hardware Software Co-Design (10MT15704) | Computer Communication Networks (10MT16104) | TCP/IP Protocols (10MT16301) |
| 01/02/13 FRI | 2.30 PM TO 4.30 PM | Process Engineering Principles (10MT10305) | Database Management Systems (10MT10505) | Reactive Power compensation & Management (10MT10705) | Software Requirements and Estimation (10MT12505) | Microcomputer System Design (10MT13805) | VLSI Technology (10MT15705) | Information and Coding techniques (10MT16105) | Database Security and Privacy (10MT16302) |
| 02/02/13 SAT | 2.30 PM TO 4.30 PM | Analytical Techniques in Biotechnology (10MT10306) | Software Testing Methodologies (10MT10509) | Power Quality (10MT10708) | Virtualization and Cloud Computing (10MT12508) | Low Power VLSI Design (10MT13808) | ASIC Design (10MT15706) | Radar Signal Processing (10MT16108) | Network Management Systems (10MT16303) |

Note: 1. Students have to bring their college Identity Cards to the examination hall ; otherwise they will not be permitted to write the examination.

    2. Examination will be on the units- I,II,III & IV.

**PRINCIPAL**

Copy to: 1) The HODs of EEE, ECE, CSE , IT & BOT with a request to circulate among the students and staff and display on the department notice board.

    2) Director, PG Courses

    3) The Examination Section

    4) A.O, for necessary transport arrangement.