



SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)
Sree Sainath Nagar, Tirupati- 517 102
(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapur)

III B.Tech II Semester Regular Examinations May, 2013

**EXAMINATION SCHEDULE FOR SUBSTITUTE SUBJECTS
FOR STUDENTS OF R07 REGULATION READMITTED TO III B.TECH II SEMESTER (SVEC 10)**

Date: 26-03-2013
Time: 10.00 AM To 01.00PM

DATE /DAY	For 2007 Admitted Batch only
	CSE
21/05/2013 TUESDAY	SYSTEM SOFTWARE 10BT51501
23/05/2013 THURSDAY	OPERATING SYSTEMS 10BT50504

PRINCIPAL

Copy to : The HOD of CSE : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.
The Examination Section.
The A.O, S.V.E.C.
SVEC Canteen. , Estate Manager, Hostel Notice Boards.