

SREE VIDYANIKETHAN ENGINEERING COLLEGE

SREE SAINATH NAGAR, A. RANGAMPET – 517 102

2nd SHIFT POLYTECHNIC

SVEC/EC/2013-14/508


06.11.2013

CIRCULAR

The III Semester (C09) Diploma Students are informed that the Unit Test-II Examinations will be conducted as per the following schedule.

DAY/ DATE	TIME	DCE	DEEE	DME	DECE	DCME
		SUBJECTS	SUBJECTS	SUBJECTS	SUBJECTS	SUBJECTS
23.11.2013 Saturday	11.00 AM To 12.00 AM	English - II C-301	English - II EE-301	English - II M-301	English - II EC-301	English - II CM-301
	03.00 PM To 04.00 PM	Engineering Mathematics -II C-302	Engineering Mathematics -II EE-302	Engineering Mathematics -II M-302	Engineering Mathematics -II EC-302	Engineering Mathematics -II CM-302
25.11.2013 Monday	11.00 AM To 12.00 AM	Strength of Materials & Theory of Structures C-303	Electrical Circuits EE-303	Engineering Mechanics M-303	Electronic Circuits -I EC-303	Basic Electrical & Electronics Engineering. CM-303
	03.00 PM To 04.00 PM	Hydraulics C-304	DC Machines & Batteries EE-304	Electrical Engg & Basic Electronics M-304	Communication Engineering EC-304	Digital Electronics & Computer Architecture CM-304
26.11.2013 Tuesday	11.00 AM To 12.00 AM	Surveying - II C-305	Electrical and Electronic Measuring Instruments EE-305	Thermal Engineering-I M-305	Digital Electronics EC-305	Data Structures through C CM-305
	03.00 PM To 04.00 PM	Construction Materials C-306	Electronics Engineering EE-306	Manufacturing Technology -I M-306	Circuit Theory EC-306	RDBMS CM-306

Copy to: 1) The Vice Principal, 2nd Shift Polytechnic: With a request to arrange to read out in the respective classes and to display on the Notice Board
2) A.O. for Information
3) The Exam Section 4) Notice Board


PRINCIPAL
