



SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)
Sree Sainath Nagar, Tirupati- 517 102
Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapur)

IV B.Tech I Semester Regular Examinations January, 2014

**EXAMINATION SCHEDULE FOR SUBSTITUTE SUBJECTS
FOR STUDENTS OF R09 REGULATION READMITTED TO III B.TECH I SEMESTER (SVEC 10)**

Date: 13-11-2013
Time: 02.00 PM To 05.00 PM

DATE /DAY	For 2009 Admitted Batch only
	IT
08/01/2014 WEDNESDAY	ARTIFICIAL INTELLIGENCE 10BT71503
20/01/2014 MONDAY	EMBEDDED SYSTEMS 10BT70506

PRINCIPAL

Copy to : The HOD of IT : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.
The Examination Section.
The A.O, S.V.E.C.
SVEC Canteen. , Estate Manager, Hostel Notice Boards.