



**SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)**  
Sree Sainath Nagar, Tirupati- 517 102  
(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapur)

**III B.Tech II Semester Supplementary Examinations January 2014**

**EXAMINATION SCHEDULE FOR SUBSTITUTE SUBJECTS  
FOR STUDENTS OF R07 REGULATION READMITTED TO III B.TECH II SEMESTER (SVEC 10)**

Date: 13-11-2013  
Time: 02.00 PM To 05.00PM

DATE /DAY	For 2007 Admitted Batch only
	CSE
22/01/2014 WEDNESDAY	SYSTEM SOFTWARE 10BT51501
24/01/2014 FRIDAY	OPERATING SYSTEMS 10BT50504

**PRINCIPAL**

Copy to : The HOD of CSE : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.  
The Examination Section.  
The A.O, S.V.E.C.  
SVEC Canteen. , Estate Manager, Hostel Notice Boards.