



SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)
Sree Sainath Nagar, Tirupati- 517 102
(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapur)

III B.Tech I Semester Regular Examinations January, 2014

EXAMINATION SCHEDULE FOR SUBSTITUTE SUBJECTS
FOR STUDENTS OF R07 REGULATION READMITTED TO III B.TECH I SEMESTER (SVEC 10)

Date: 13-11-2013
Time: 10.00 AM To 01.00PM

DATE /DAY	For 2008 Admitted Batch only
	CSE
16/01/2014 THURSDAY	SYSTEM SOFTWARE 10BT51501
18/01/2014 SATURDAY	COMPUTER GRAPHICS 10BT51202


PRINCIPAL

Copy to : The HOD of CSE : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.
The Examination Section.
The A.O, S.V.E.C.
SVEC Canteen. , Estate Manager, Hostel Notice Boards.