SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)
Sree Sainath Nagar, A. Rangampet – 517 102

M. Tech I Semester Mid-II Examinations will be conducted as per the schedule given below.

TIME-TABLE

Time: 02:30 PM to 04:30 PM   25.01.2014

DATE /DAY	Bio-Technology (BOT) (03)	Computer Science (CS) (05)	Electrical Power Systems (EPS) (07)	Software Engineering(SE) (25)	Digital Electronics and Communication Systems (DECS) (38)	VLSI (57)	Communication Systems (CMS) (61)	Computer Networks and Information Security (CN&IS) (63)
10/02/14 MON	General Microbiology and Biochemistry (10MT10301)	Computer Architecture (10MT10501)	Advanced Control Systems (10MT10701)	Advanced Data Structures and Algorithms (10MT12501)	Advanced Digital Signal Processing (10MT13801)	Analog IC Design (10MT15701)	Advanced Mathematics for Communication Systems (10MT16101)	Software Security Engineering (10MT22504)
11/02/14 TUE	Molecular biology (10MT10302)	Discrete Structures and Automata Theory (10MT10502)	EHVAC Transmission (10MT10702)	Advanced Software Engineering (10MT12502)	Digital Communication Techniques (10MT13802)	Digital Design Modeling and Synthesis with HDLs (10MT15702)	Modern Digital Communication Techniques (10MT16102)	Data warehousing and Data Mining (10MT20502)
12/02/14 WED	Microbial Engineering (10MT10303)	Data Structures and Algorithms (10MT10503)	Microprocessors & Microcontrollers (10MT10703)	Middleware Technologies (10MT12503)	Digital System Design (10MT13803)	Digital IC Design (10MT15703)	Detection and Estimation of Signals (10MT23803)	Data Structures and Algorithms (10MT10503)
13/02/14 THU	Immunology (10MT10304)	Software Engineering (10MT10504)	Power System Control & Stability (10MT10704)	Software Metrics (10MT12504)	Embedded Systems (10MT13804)	Hardware Software Co-Design (10MT15704)	Computer Communication Networks (10MT16104)	TCP/IP Protocols (10MT16301)
14/02/14 FRI	Process Engineering Principles (10MT10305)	Database Management Systems (10MT10505)	Reactive Power compensation & Management (10MT10705)	Software Requirements and Estimation (10MT12505)	Microcomputer System Design (10MT13805)	VLSI Technology (10MT15705)	Information and Coding techniques (10MT16105)	Database Security and Privacy (10MT16302)
15/02/14 SAT	Analytical Techniques in Biotechnology (10MT10306)	Software Testing Methodologies (10MT10509)	Power Quality (10MT10708)	Virtualization and Cloud Computing (10MT12508)	DSP Processors and Architectures (10MT13807)	ASIC Design (10MT15706)	Radar Signal Processing (10MT16108)	Network Management Systems (10MT16303)

Note: 1.Students have to bring their college Identity Cards to the examination hall ; otherwise they will not be permitted to write the examination.

2. Examination will be on the units- V,VI,VII & VIII.

PRINCIPAL

Copy to: 1) The Director,SVEC .
2) The HODs of EEE, ECE, CSE , IT & BOT with a request to circulate among the students and staff and display on the department notice board.
3) The Examination Section
4) A.O, for necessary transport arrangement.