

SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)
Sree Sainath Nagar, Tirupati- 517 102
(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

III B.Tech II Semester Regular Examinations June, 2014

EXAMINATION SCHEDULE FOR SUBSTITUTE SUBJECTS
FOR STUDENTS OF R07 REGULATION READMITTED TO III B.TECH II SEMESTER (SVEC 10)

Date: 15-04-2014
Time: 10.00 AM To 01.00PM

DATE /DAY	For 2007 Admitted Batch only
	CSE
05/06/2014 THURSDAY	MODELING AND SIMULATION 10BT61501


PRINCIPAL


Copy to : The HOD of CSE : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.
The Examination Section.
The A.O, S.V.E.C.
SVEC Canteen , Estate Manager, Hostel Notice Boards.