SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati- 517 102 (Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

IV B.Tech I Semester Supplementary Examinations June, 2014

EXAMINATION SCHEDULE FOR SUBSTITUTE SUBJECTS FOR STUDENTS OF <u>R09</u> REGULATION READMITTED TO III B.TECH I SEMESTER (SVEC 10)

Date: 15-04-2014

Time: 02.00 PM To 05.00 PM

DATE /DAY	For 2009 Admitted Batch only
	IT
04/06/2014 WEDNESDAY	ARTIFICIAL INTELLIGENCE 10BT71503
13/06/2014 FRIDAY	EMBEDDED SYSTEMS 10BT70506

Copy to: The HOD of IT: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board. The Examination Section.

The A.O, S.V.E.C.

SVEC Canteen., Estate Manager, Hostel Notice Boards.