

SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)
Sree Sainath Nagar, Tirupati- 517 102
(Affiliated to Jawaharlal Nehru Technological University Anantapur, Ananthapuramu)

III B.Tech I Semester Regular Examinations November, 2014

EXAMINATION SCHEDULE FOR SUBSTITUTE SUBJECTS
FOR STUDENTS OF R07REGULATION READMITTED TO III B.TECH I SEMESTER (SVEC 10)

Date: 25-09-2014
Time: 10.00 AM To 01.00PM

DATE /DAY	For 2008 Admitted Batch only
	CSE
02/12/2014 TUESDAY	SYSTEM SOFTWARE 10BT51501
06/12/2014 SATURDAY	COMPUTER GRAPHICS 10BT51202


PRINCIPAL

Copy to : The Director, SVEC

The HOD of CSE : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The A.O, S.V.E.C.

SVEC Canteen, Estate Manager, Hostel Notice Boards.